

### Amendments To Claims

1. (currently amended) A low visual noise, jitterized pulse width modulation brightness control circuit, for adjusting a brightness of a fluorescent lamp in a liquid crystal display comprising:

a brightness control signal generating unit receiving a brightness adjusting signal and generating a brightness control signal in response to said brightness adjusting signal, said brightness control signal having an operational period varying in a predetermined range, wherein said brightness control signal drives the fluorescent lamp so that back light signals generated by the fluorescent lamp have the same frequency as vertical scanning signals and horizontal scanning signals; and

an inverter coupled to said brightness control signal generating unit driving said fluorescent lamp in response to said brightness control signal.

2. (currently amended) The circuit of claim 1, wherein said brightness control signal generating unit comprises:

a noise generator generating a noise;

an analog adder, coupled to said noise generator, receiving and adding said brightness adjusting signal and said noise; and

a comparator, couple to said analog adder, comparing said added said brightness adjusting signal and said noise and a triangle wave to generate said brightness control signal.

3. (original) The circuit of claim 2, wherein said noise level is adjustable.

4. (currently amended) A low visual noise, jitterized pulse width modulation brightness control circuit, for adjusting a brightness of a fluorescent lamp in a liquid crystal display comprising:

a brightness control signal generating unit receiving a brightness adjusting signal and generating a brightness control signal in response to said brightness adjusting signal, said brightness control signal having an operational frequency varying in a predetermined range, wherein said brightness control signal drives the fluorescent lamp so that back light signals generated by the fluorescent lamp have the same frequency as vertical scanning signals and

horizontal scanning signals; and

a an inverter coupled to said brightness control signal generating unit driving said fluorescent lamp in response to said brightness control signal

5. (original) The circuit of claim 4, wherein said brightness control signal generating unit is a microprocessor.

6. (original) The circuit of claim 4, wherein said brightness control signal has a phase varying in a predetermined range.

7. (new) A low visual noise, jitterized pulse width modulation brightness control circuit, for adjusting a brightness of a fluorescent lamp in a liquid crystal display comprising:

a brightness control signal generating unit receiving a brightness adjusting signal and generating a brightness control signal in response to said brightness adjusting signal, said brightness control signal having an operational period varying in a predetermined range; and

an inverter coupled to said brightness control signal generating unit driving said fluorescent lamp in response to said brightness control signal;

wherein said brightness control signal generating unit comprises:

a noise generator generating a noise;

an analog adder, coupled to said noise generator, receiving and adding said brightness adjusting signal and said noise; and

a comparator, couple to said analog adder, comparing said added brightness adjusting signal and said noise and a triangle wave to generate said brightness control signal.